● PRINTER RUSH ● (PTO ASSISTANCE)

Application :	194 Examiner: 🔎	4. PALADINI	GAU:	2125		
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[RUSH] MESSAGE: PLEASE PROVIDE SERIAL # 'S FOR THE 4 ATTORNEY DOCKET #'S ON PAGE 2 OF SPECIFICATION. THANK YOU						
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REV 10/04

METHOD AND APPARATUS FOR IMPLEMENTING A METAMETHODOLOGY

CROSS REFERENCE TO RELATED APPLICATIONS

[0001] The present application hereby incorporates the following United Stated Patent Applications by reference in their entirety:

224-20	Attorney Docket Number	P.N./Express Mail Label Number	Filing Date
098 4233	LSI 01-695-	-09/842,335	April 25, 2001
10021414	LSI 01-488	EV 013 245 395 US	October 30, 2001
1002/6/9		EV 013 245 404 US	October 30, 2001
10021690		EV 013 245 418 US	October 30, 2001

FIELD OF THE INVENTION

[0002] The present invention generally relates to the field of product design, and particularly to method and apparatus for implementing a metamethodology in integrated circuit (IC) design.

BACKGROUND OF THE INVENTION

[0003] Integrated circuits have become a necessary part of everyday modern society. From cell phones and computers, to household appliances and automobiles, a wide range of integrated circuits are utilized to provide a broad range of functionality. To provide this functionality, integrated circuits may need to be specialized to have the functions necessary to achieve the desired results. Additionally, more and more functions are being included within each integrated circuit.

[0004] However, this has resulted in a level of design complexity previously unimagined. For example, to achieve the functions desired, multiple vendors may be used to provide Electronic Design Automation (EDA) software tools. The variety of such tools is supplied from a plurality of sources; this results in compatibility and management issues.